| <ul><li>(a) DPTR</li><li>(b) Accumulator</li></ul>  |  | No. of Printed Pages : 4 Roll No   |  |  |  |  |
|---|--|--|--|--|--|--|
| (c) SP  |  | 5th Sem. / Medical Electronics   |  |  |  |  |
| disabled.   | upts can be enabled or   | Subject: MICROCONTROLLER AND EMBEDDED SYSTEM Time: 3 Hrs.  SECTION-A   |  |  |  |  |
| Q.32 Write Five important t<br>SECT   |  | <b>Note:</b> Objective type questions. All questions a compulsory (10x1=1)   |  |  |  |  |
| questions out of four Q.33 Explain architecture with the help of neat to Q.34 With the help of interfacing of Seve 8051.  Q.35 Define Embedded different applications | e of 8051 microcontroller block diagram.  neat diagram explain en segment Display with  system and explain its  s.  e functional block diagram | Q.1 8051 is abit Microcontroller.  Q.2 RISC stands for  Q.3 8051 hassize internal RAM.  Q.4 Number of input output ports 8051 are  Q.5 DAA stands for  Q.6 RESET pin in 8051 is active low input pin (True/false)  Q.7 Write one instruction from immediate addressing mode. |  |  |  |  |
| 100) (4   | 123252   | (1) 12325  |  |  |  |  |

| Q.8  | RESET interrupt in 8051 is maskable i  | nterrupt. | Q.18   | Define assem               | bler.         |                |                     |
|--|--|-----------|--|----------------------------|---------------|----------------|---------------------|
|  | (True/False)   |           | Q.19   | Differentiate              | between       | microproce     | ssor ar             |
|  | When PUSH instruction is executed th   |           |  | microcontrolle             |               | - 4            |                     |
|  | pointer(increments/decrem  |           |  | Draw PSW reg               |               | )1.            |                     |
| ∩ 10 s   | 8051 has two bit up-counters.  |           |  | Define interfac            | · ·           |                |                     |
| Q. 10  | bit up-counters.   |           | Q.22   | why 8051 is ca             | alled 8-bit m | nicrocontrolle | ∋r.                 |
| SECTION-B  |  |           | SECTION-C  |                            |               |                |                     |
| Note   | Very Short answer type questions. Atte<br>ten questions out of twelve questions .1 |           |  | Short answer questions out | • • •         | •              | t any eig<br>8x5=40 |
|  | terrquestions out or twelve questions.   | 0,2-20    | Q.23   | Explain memo               | ory organiza  | ation of 8051  |                     |
| Q.11   | Define Microcontroller.  |           | Q.24   | Explain interfa            | acing of A/   | D converter    | with 805            |
| Q.12 Name Interrupts of 8051.  |  |           | microcontroller.   |                            |               |                |                     |
| Q.13 Write the function of following pins.   |  |           | Q.25 Write short note on applications o microcontroller in communication system. |                            |               |                |                     |
| (A)  | PSEN   |           | Q.26   | What do you                | ı mean by     | assembler of   | directives          |
| (D)  | A1 E   |           |  | Explain DB (D              | efine byte)   | assembler d    | lirective.          |
| (B)ALE   |  |           | Q.27 What do you understand by Edge triggered and                                |                            |               |                |                     |
| Q.14 List different addressing modes of 8051.                                      |  |           |  | level triggered            | dinterrupts   | •              |                     |
| Q.15 Define Embedded system.  Q.16 Give two example of data transfer instructions. |  |           | Q.28   | Draw the pin d             | liagram of 8  | 3051.          |                     |
|  |  |           | Q.29 Define addressing mode. Explain immediate                                   |                            |               |                |                     |
| Q. 10  | Give two example of data transfer instit   | ictions.  | and indirect addressing mode of 8051.  |                            |               |                |                     |
| Q.17   | Explain how a timer mode can be set?   |           | Q.30   | Write function             | of following  | <b>g</b> :     |                     |
|  | (2)  | 123252    |  |                            | (3)           |                | 12325               |
|  |  |           |  |                            |               |                |                     |