

## SECTION-D

**Note:** Long answer type questions. Attempt any three questions out of four questions. 3x10=30

- Q.33 With a neat sketch explain the block diagram of 8085 microprocessor.
- Q.34 Explain in detail the process of servicing an interrupt in 8085
- Q.35 With a neat sketch explain the block diagram of 8255 programmable peripheral interface.
- Q.36 Write a program to add two sixteen bit number stored in continuous memory location from 3000H. Store the result in memory location starting from 3050H.

No. of Printed Pages : 4

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**5th Sem.**

**Subject : Microprocessor**

Time : 3 Hrs.

M.M. : 100

## SECTION-A

**Note:** Objective type questions. All questions are compulsory (10x1=10)

- Q.1 Address bus of 8085 is \_\_\_\_\_ bit wide.
- Q.2 The clock frequency of 8085 is \_\_\_\_\_ Hz.
- Q.3 Which register pair is used as memory pointer in 8085.
- Q.4 Define wait state.
- Q.5 Define memory map.
- Q.6 Define T-state.
- Q.7 Name the highest priority interrupt of 8085
- Q.8 Expand PROM
- Q.9 Vectored address of TRAP interrupt is \_\_\_\_\_.

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Q.10 Which IC is commonly used as universal synchronous asynchronous receiver transmitter.

### SECTION-B

**Note:**Very short answer type questions. Attempt any ten questions out of twelve questions. 10x2=20

Q.11 Explain two application of microprocessor.

Q.12 Draw the flag register of 8085.

Q.13 Which flags are effected by ORI instruction.

Q.14 Explain partitioning of total memory space.

Q.15 Define Opcode.

Q.16 Explain DAA instruction.

Q.17 Explain the uses of stack

Q.18 Describe the function of Hold pin.

Q.19 Give the CWR of 8255.

Q.20 Explain the RIM instruction.

Q.21 Define software interrupt.

Q.22 Explain the significance of NMI.

### SECTION-C

**Note:**Short answer type questions. Attempt any eight questions out of ten questions. 8x5=40

Q.23 Explain de-multiplexing of address data bus.

Q.24 Explain the steps for executing a program.

Q.25 Differentiate between Instruction cycle and machine cycle.

Q.26 List five difference between memory mapped IO and Peripheral mapped IO

Q.27 Differentiate between jump and call instructions.

Q.28 Explain the register addressing mode and indirect addressing modes of 8085 with example.

Q.29 Write a program to subtraction of 8-bit number.

Q.30 Explain SHLD and XCHG instructions.

Q.31 Explain the DMA operation.

Q.32 Explain the address decoding of memories.